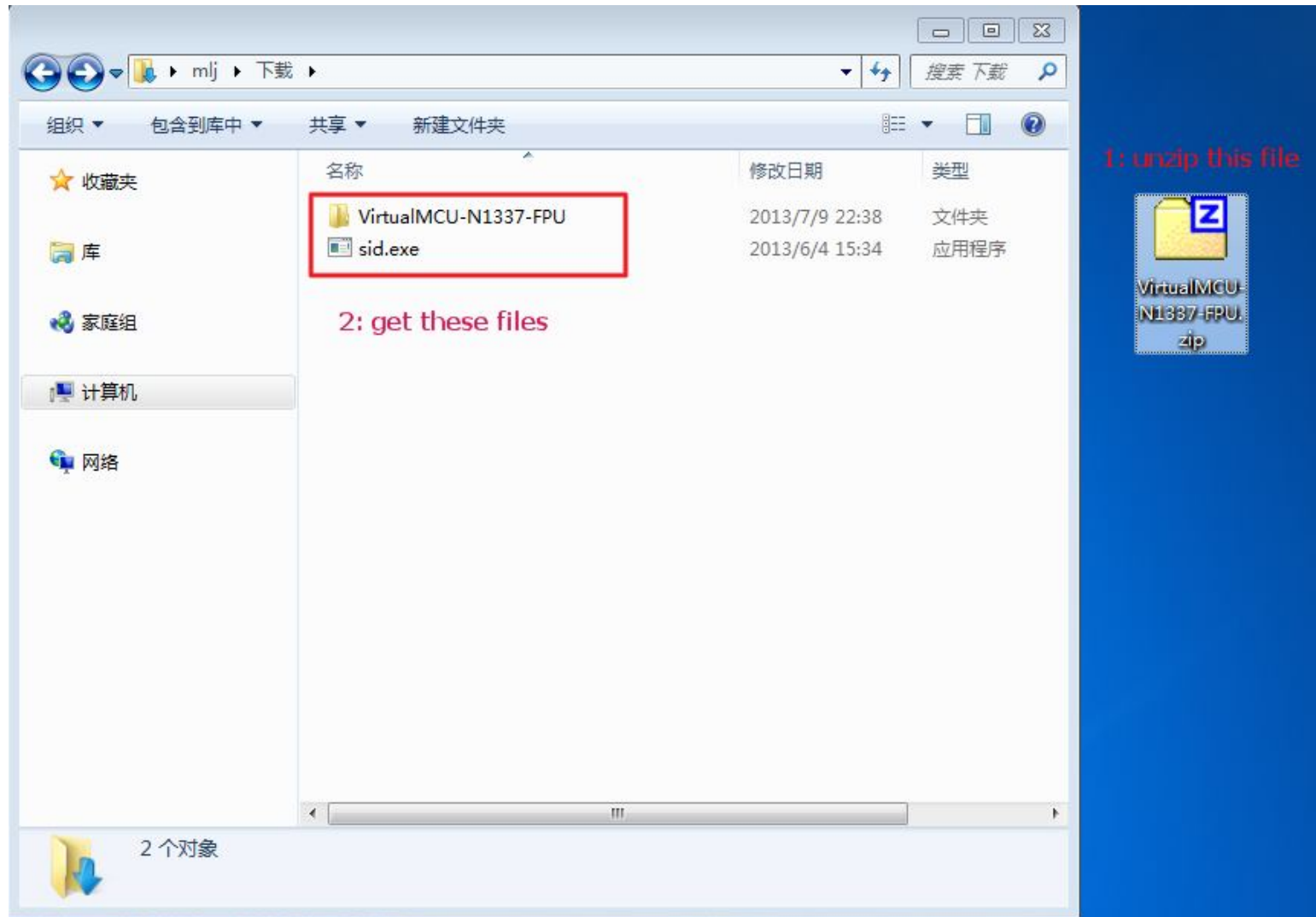


Add N1337 to AndeSight 201 MCU

Andes Technology



Uncompress



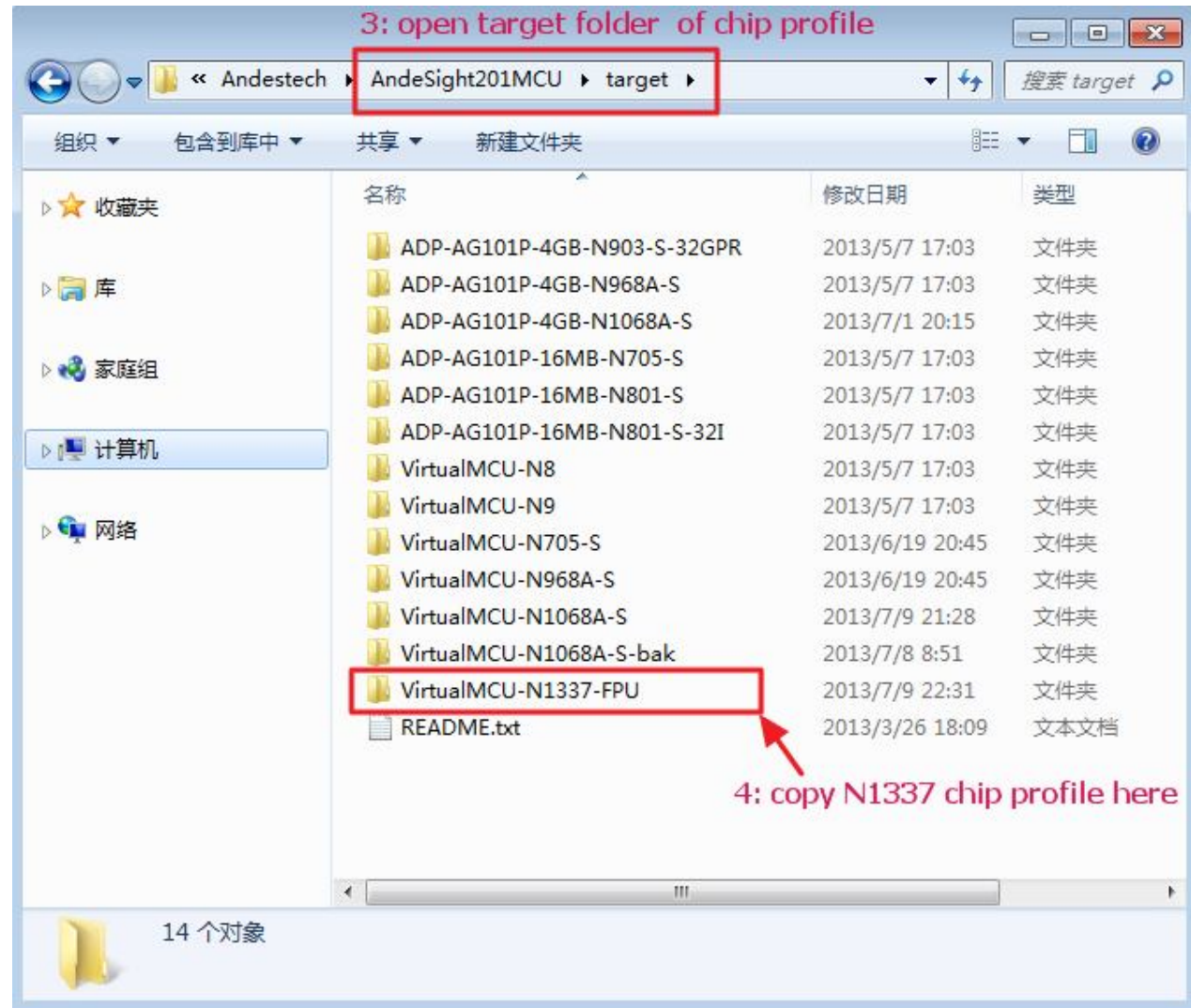
名称	修改日期	类型
VirtualMCU-N1337-FPU	2013/7/9 22:38	文件夹
sid.exe	2013/6/4 15:34	应用程序

2: get these files

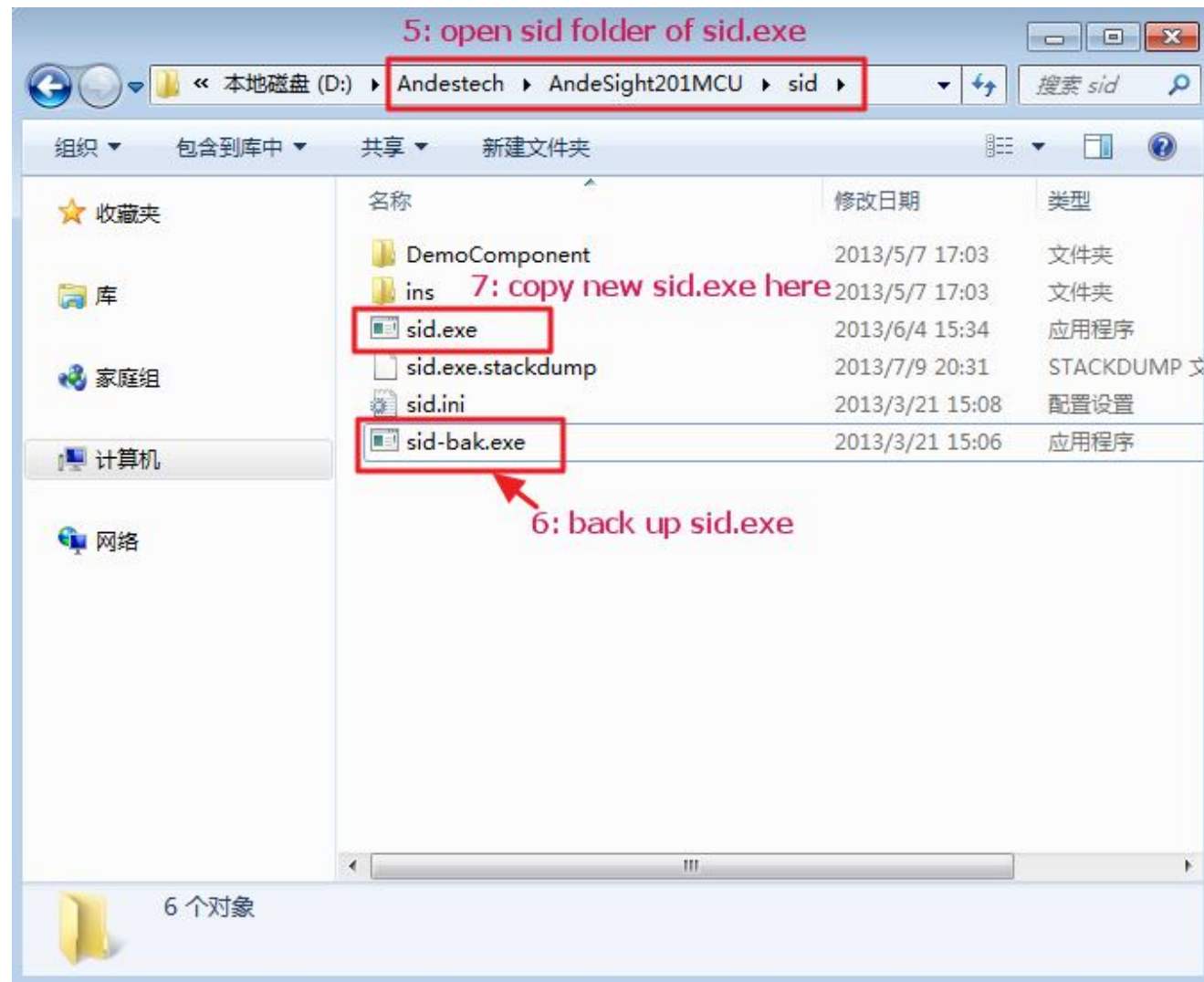
1: unzip this file

VirtualMCU-N1337-FPU.zip

Copy chip profile



Back up sid then copy new side



Now N1337 chip is available



C/C++ - AndeSight MCU Version

File Edit Navigate Search Run Project Window Help

Target Manager ... djpeg.c jmemnobs.c heap.c Andes Project Cre... main() at d:\and... lcdc.c

Build on resource save

Project Language
☒ C ☐ C++

Connection Type
☒ Simulator ☐ AICE

Target Chip

8: reopen AndeSight201MCU, now there is

Name	Chip	CPU	Simulator Config
ADP-AG101P-16MB-N705-S	ADP-AG101P-16MB-N705-S	[N705-S]	ADP-XC5-for-N705-S-16M.vep
ADP-AG101P-16MB-N801-S	ADP-AG101P-16MB-N801-S	[N801-S]	ADP-XC5-for-N801-S-16M.vep
ADP-AG101P-16MB-N801-S-32I	ADP-AG101P-16MB-N801-S-32I	[N801-S]	ADP-XC5-for-N801-S-32I-16M.vep
ADP-AG101P-4GB-N1068A-S	ADP-AG101P-4GB-N1068A-S	[N1068A-S]	ADP-XC5-for-N1068A-S.vep
ADP-AG101P-4GB-N903-S-32GPR	ADP-AG101P-4GB-N903-S-32GPR	[N903-S]	ADP-XC5-for-N903A-S.vep
ADP-AG101P-4GB-N968A-S	ADP-AG101P-4GB-N968A-S	[N968A-S]	ADP-XC5-for-N968A-S.vep
VirtualMCU-N1068A-S	ADP-AG101P-4GB-N1068A-S	[N1068A-S]	VirtualMCU_N1068A_S.vep
VirtualMCU-N1068A-S-bak	ADP-AG101P-4GB-N1068A-S	[N1068A-S]	VirtualMCU-N1068A-S.vep
VirtualMCU-N1337-FPU	ADP-AG101P-4GB-N1337-FPU	[N1337-FPU]	VirtualMCU_N1337_FPU.vep
VirtualMCU-N705-S	ADP-AG101P-16MB-N705-S	[N705-S]	VirtualMCU-N705-S.vep
VirtualMCU-N8	ADP-AG101P-16MB-N801-S	[N801-S]	VirtualMCU-N8.vep
VirtualMCU-N9	ADP-AG101P-4GB-N903-S-32GPR	[N903-S]	VirtualMCU-N9.vep
VirtualMCU-N968A-S	ADP-AG101P-4GB-N968A-S-32GPR	[N968A-S]	VirtualMCU-N968A-S.vep

Project Creator Preferences

Console
<terminated> JPEG.adx [(DSF) C/C++ Application] JPEG.adx



Thank You !

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