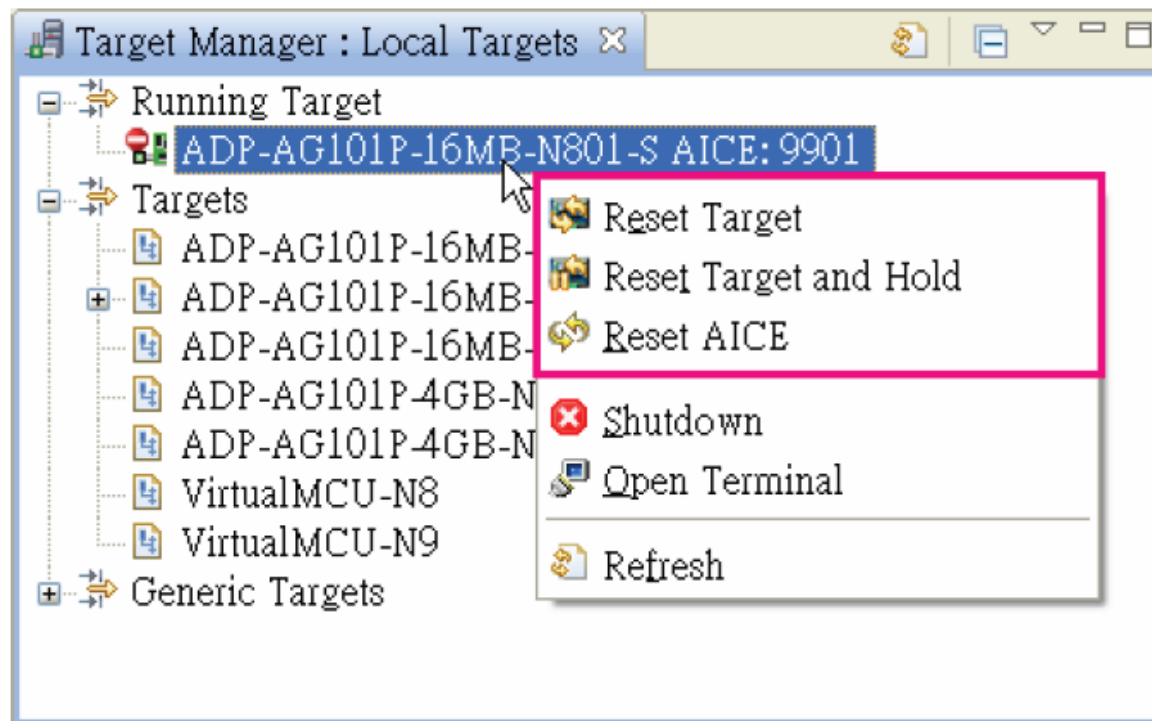


Resets in Running Target



Resets in AndeSight

AndeSight	Andes ICE Box	JTAG Connector	Which module would be reset?	Action after reset
Reset Target	ICE Box is NOT reset	nSRST is triggered	Depends on the implementation. Normally, the core and bus clock domains are reset.	Free run
Reset Target and Hold			The TCK domain modules are NOT reset.	CPU stops at first instruction and enters debug mode.
Reset AICE	ICE Box is reset	TRST is triggered	The TCK domain modules are reset.	Free run

Thank You!



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