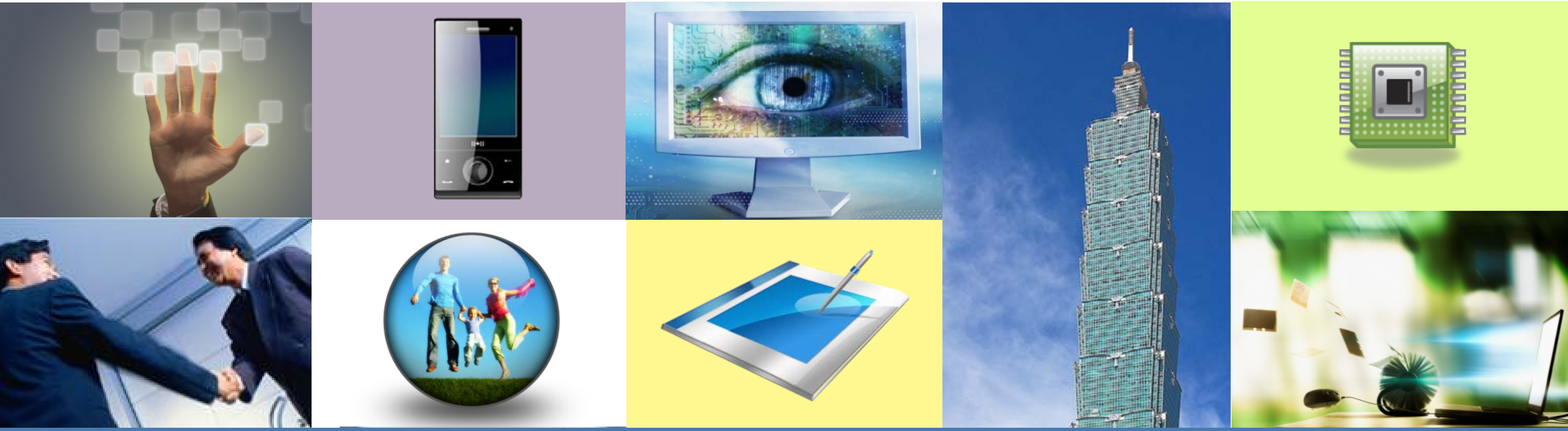
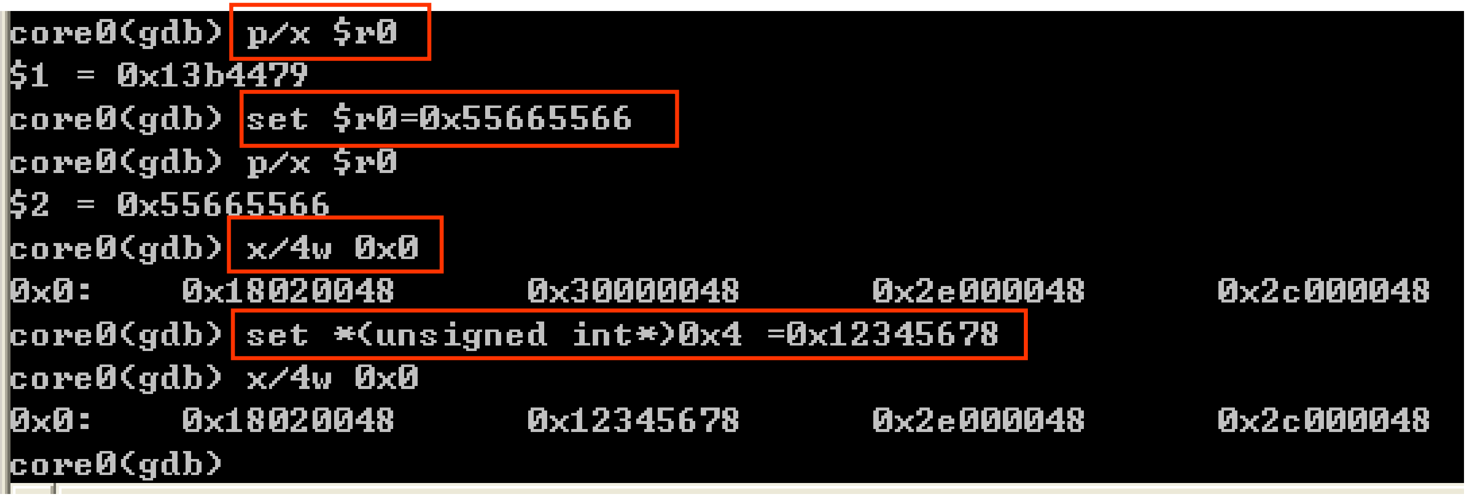
Pre-config Target Before Load Code



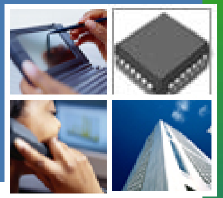
GDB Command – Read/Write Register and Memory

- (gdb) p/x $r0 – Print register
- (gdb) set $r0=0x55665566 – Set register
- (gdb) x/4w 0x0
  - – Examine memory (4w→4 words, 0x0→address)
- (gdb) set *(unsigned int*) 0x4=0x12345678
  - – Set memory
- (gdb) p variable – Print variable

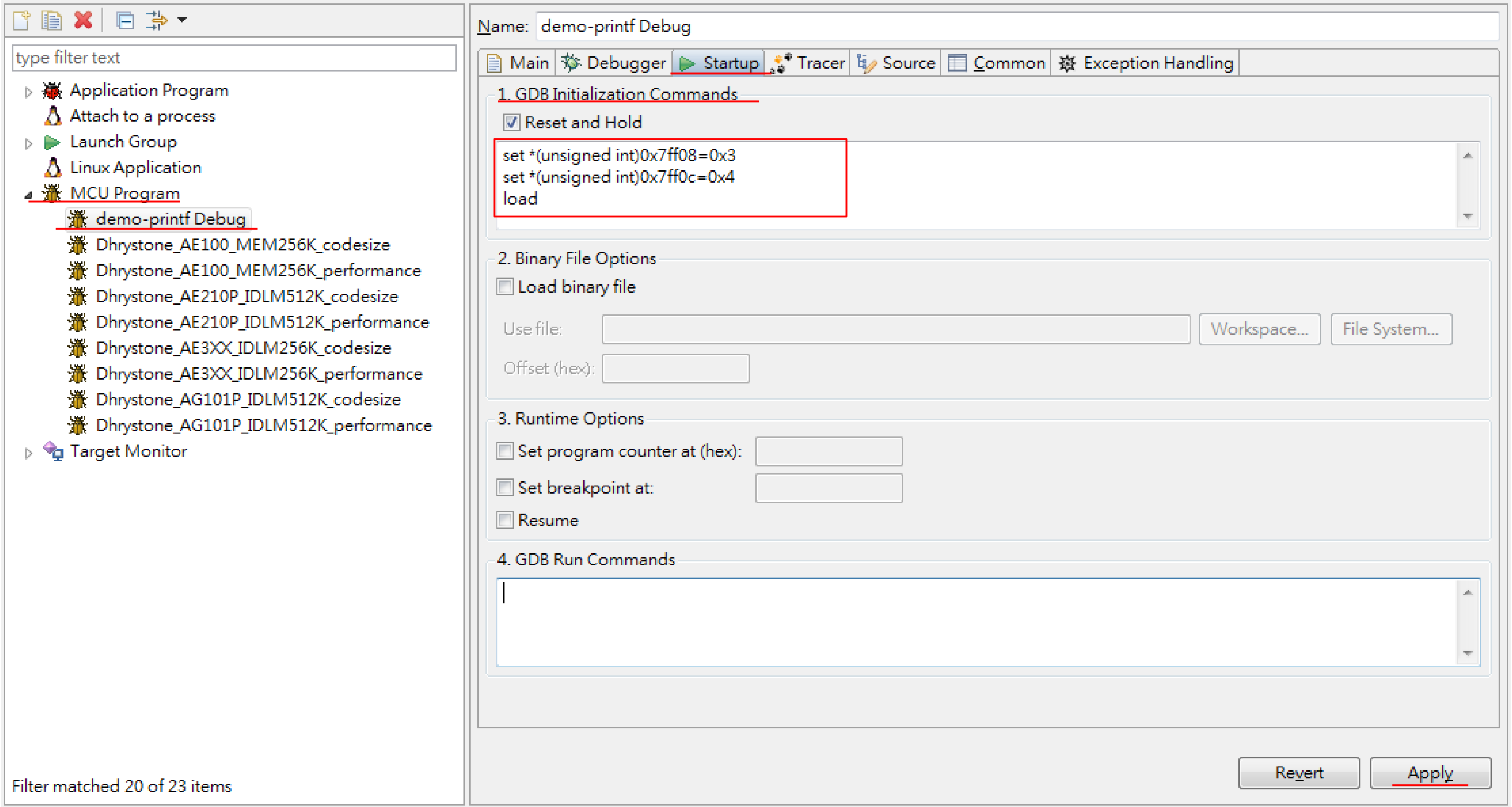


```
core0(gdb) p/x $r0
$1 = 0x13b4479
core0(gdb) set $r0=0x55665566
core0(gdb) p/x $r0
$2 = 0x55665566
core0(gdb) x/4w 0x0
0x0:    0x18020048      0x30000048      0x2e000048      0x2c000048
core0(gdb) set *(unsigned int*)0x4 =0x12345678
core0(gdb) x/4w 0x0
0x0:    0x18020048      0x12345678      0x2e000048      0x2c000048
core0(gdb)
```

AndeSight Configurations



- In the project Debug As->Debug Configurations-> Select your project in MCU Program->"Startup" tab ->check "Reset and Hold"->set *(unsigned int)0x7ff08=0x3, ..., last command is load->Apply



type filter text

- Application Program
- Attach to a process
- Launch Group
- Linux Application
- MCU Program
  - demo-printf Debug
  - Dhrystone_AE100_MEM256K_codesize
  - Dhrystone_AE100_MEM256K_performance
  - Dhrystone_AE210P_IDLM512K_codesize
  - Dhrystone_AE210P_IDLM512K_performance
  - Dhrystone_AE3XX_IDLM256K_codesize
  - Dhrystone_AE3XX_IDLM256K_performance
  - Dhrystone_AG101P_IDLM512K_codesize
  - Dhrystone_AG101P_IDLM512K_performance
- Target Monitor

Filter matched 20 of 23 items

Name: demo-printf Debug

1. GDB Initialization Commands

☑ Reset and Hold

```
set *(unsigned int)0x7ff08=0x3
set *(unsigned int)0x7ff0c=0x4
load
```

2. Binary File Options

☐ Load binary file

Use file:

Offset (hex):

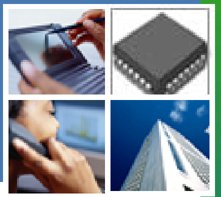
3. Runtime Options

☐ Set program counter at (hex):

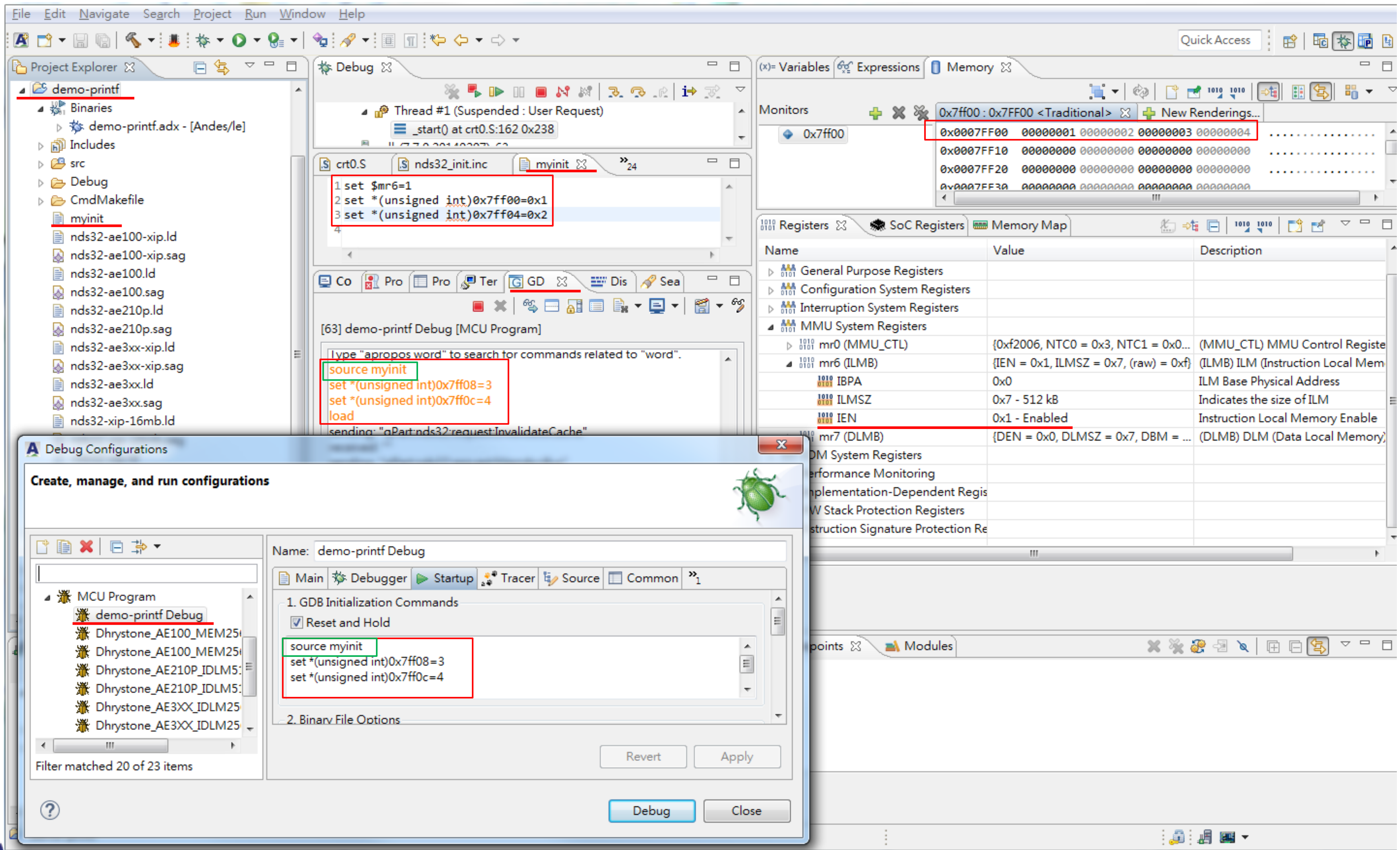
☐ Set breakpoint at:

☐ Resume

4. GDB Run Commands



❖ “source filename” also available



myinit:

```
set $mr6=1
set *(unsigned int)0x7ff00=0x1
set *(unsigned int)0x7ff04=0x2
```

[63] demo-printf Debug [MCU Program]

```
Type "apropos word" to search for commands related to "word".
source myinit
set *(unsigned int)0x7ff08=3
set *(unsigned int)0x7ff0c=4
load
sending: "qPart:nds32:request:InvalidateCache"
```

```
0x0007FF00  00000001 00000002 00000003 00000004  ................
0x0007FF10  00000000 00000000 00000000 00000000  ................
0x0007FF20  00000000 00000000 00000000 00000000  ................
0x0007FF30  00000000 00000000 00000000 00000000
```

Name	Value	Description
General Purpose Registers		
Configuration System Registers		
Interruption System Registers		
MMU System Registers		
mr0 (MMU_CTL)	{0xf2006, NTC0 = 0x3, NTC1 = 0x0...	(MMU_CTL) MMU Control Registe
mr6 (ILMB)	{IEN = 0x1, ILMSZ = 0x7, (raw) = 0xf}	(ILMB) ILM (Instruction Local Mem
IBPA	0x0	ILM Base Physical Address
ILMSZ	0x7 - 512 kB	Indicates the size of ILM
IEN	0x1 - Enabled	Instruction Local Memory Enable
mr7 (DLMB)	{DEN = 0x0, DLMSZ = 0x7, DBM = ...	(DLMB) DLM (Data Local Memory)
DM System Registers		
rformance Monitoring		
nplementation-Dependent Regis		
W Stack Protection Registers		
struction Signature Protection Re		

Debug Configurations

Create, manage, and run configurations

MCU Program
- demo-printf Debug
- Dhrystone_AE100_MEM25
- Dhrystone_AE100_MEM25
- Dhrystone_AE210P_IDLM5
- Dhrystone_AE210P_IDLM5
- Dhrystone_AE3XX_IDLM25
- Dhrystone_AE3XX_IDLM25

Filter matched 20 of 23 items

Name: demo-printf Debug

Main | Debugger | Startup | Tracer | Source | Common

1. GDB Initialization Commands

☑ Reset and Hold

```
source myinit
set *(unsigned int)0x7ff08=3
set *(unsigned int)0x7ff0c=4
```

2. Binary File Options