

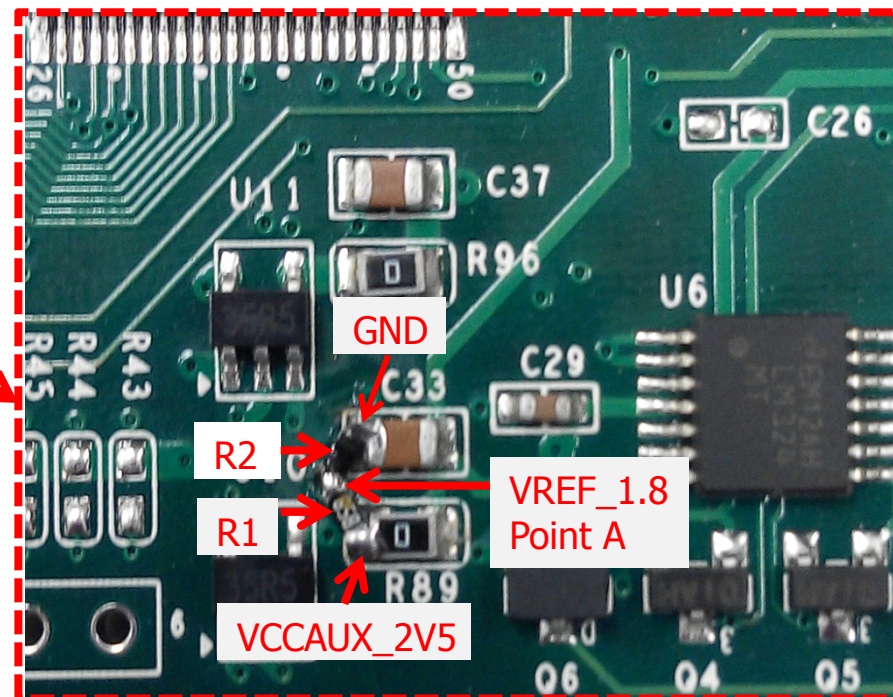
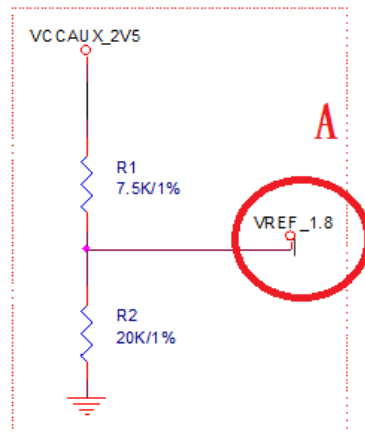
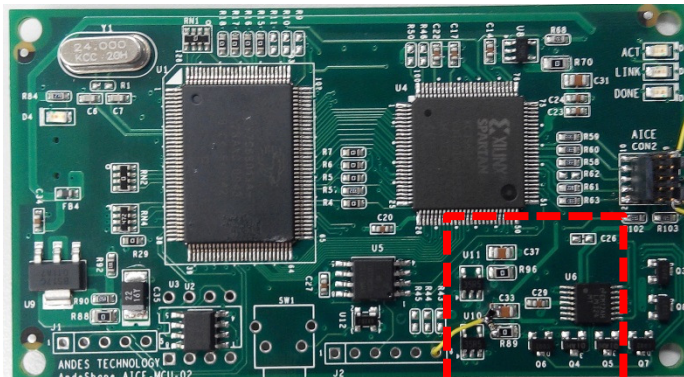
Workaround of AICE-MCU for 1.8V IO Voltage

Andes Presents The Best

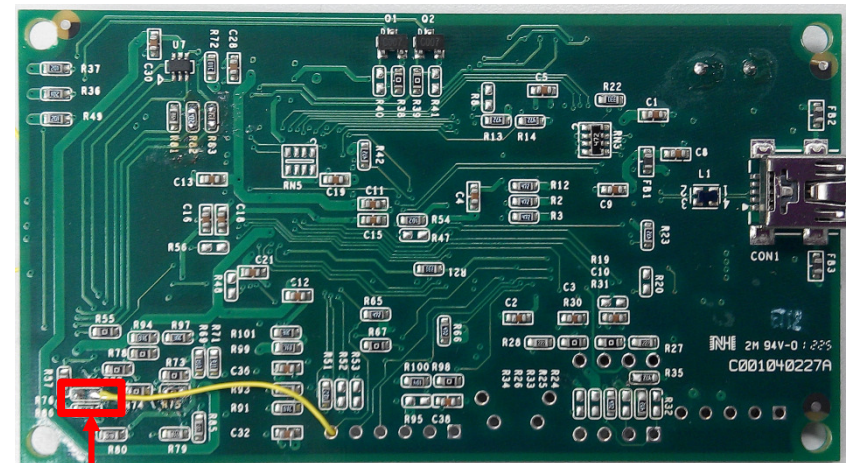
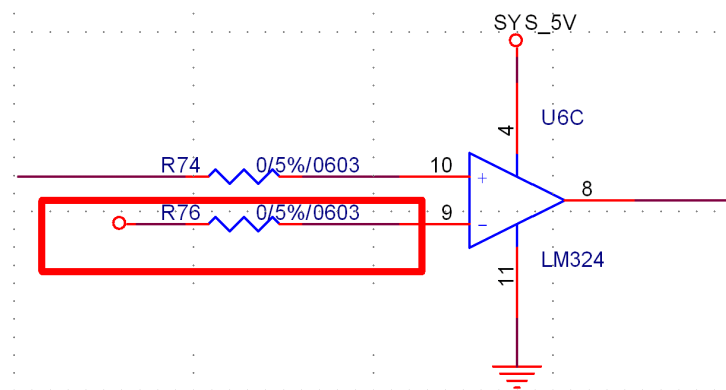


Architecture for **N**ext-generation
Digital **E**ngines for **S**oC

Step 1: Add a new 1.8V reference voltage (VERF_1.8/Point A)

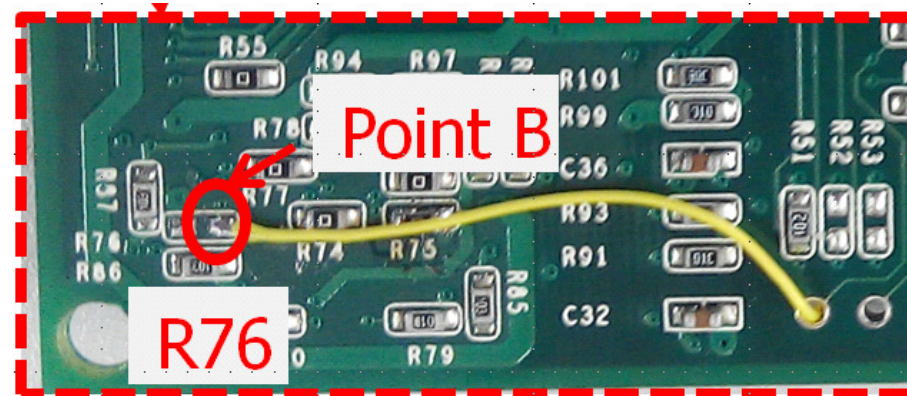
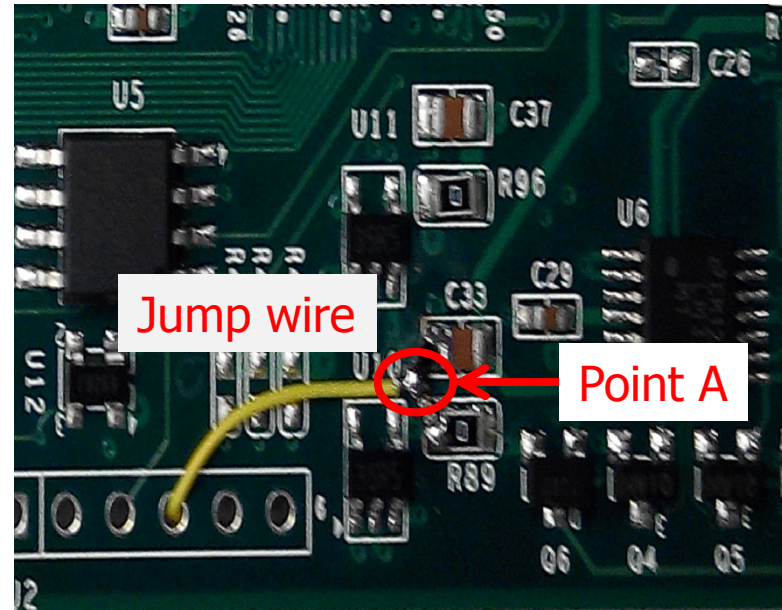
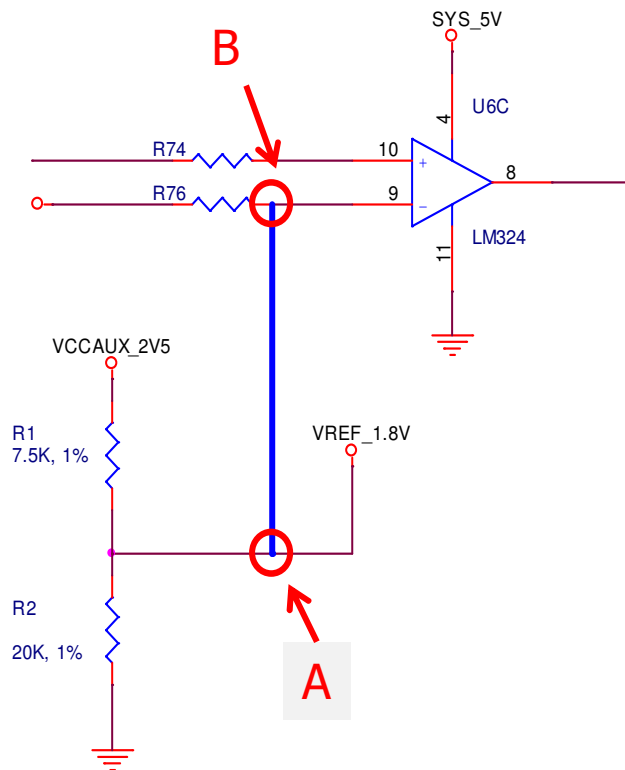


Step 2: Remove R76 resistor

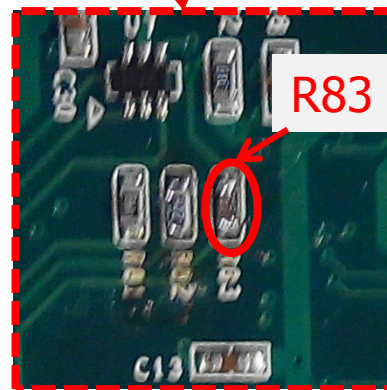
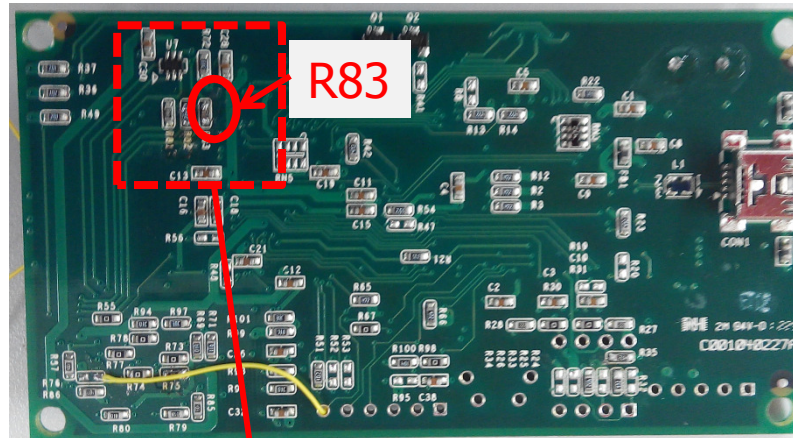


Remove R76 resistor

Step 3: Connect a wire from Point A to Point B



Step 4: Change R83 resistor value from 20K ohm to 8.2K ohm, 1%



Thank You!!!



Architecture for **N**ext-generation
Digital **E**ngines for **S**oC